

ABSTRACT OF THE DISCLOSURE

A circuit design method to control access pointers of different memory used to transfer the data buffer of printers. The proposed control signal circuit employs a read control signal circuit and a write control signal circuit to receive and send $m+n$ -bit control signals, respectively, to control the access pointers of a memory with $2^m * 2^n$ -register to perform buffer transfer in printers. The disclosed method is able to solve the problem that only one memory block is accessible in sequential mode before. In addition, the method is able to use all memory blocks in buffer transfer in sequential mode to increases the efficiency of data buffering when printing.